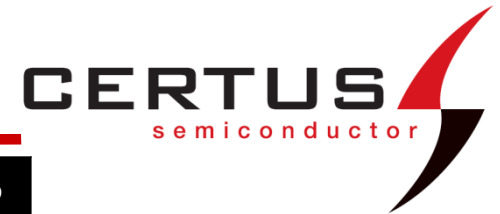


Process Technologies IP and Experience



Process Technology	Full Custom IO and Padlib Library IP	Customized IO Designs	Full ESD Library IP	Custom ESD Solutions	Consultation: IO and ESD Design	Consultation: ESD Debug Efforts
TSMC 28nm	Yes	Yes	Yes	Yes	Yes	Yes
TSMC 40nm	No	No	No	Yes	Yes	Yes
TSMC 65nm	No	Yes	Yes	Yes	Yes	Yes
TSMC 90nm	No	No	No	Yes	Yes	Yes
TSMC 130nm	No	Yes	Yes	Yes	Yes	Yes
TSMC 180nm	No	Yes	Yes	Yes	Yes	Yes
TSMC 250nm	No	No	Yes	Yes	Yes	Yes
GF 45nm	No	No	No	No	Yes	Yes
GF 65nm	Yes	Yes	Yes	Yes	Yes	Yes
GF 180nm	Yes	No	Yes	Yes	Yes	Yes
SMIC 40nm	No	Yes	No	Yes	Yes	Yes
SMIC 65nm	No	No	No	Yes	Yes	Yes
IBM SOI/RF 180nm	No	Yes	No	Yes	Yes	Yes
IBM/Samsung 32nm	No	Yes	No	Yes	Yes	Yes
IBM/Samsung 45nm	No	Yes	No	Yes	Yes	Yes
UMC 55nm	No	No	No	No	Yes	Yes
Peregrine/Silanna 250nm	No	No	Yes	Yes	Yes	Yes
Peregrine/Silanna 500nm	No	No	Yes	Yes	Yes	Yes
Lfoundry 110nm	Yes	Yes	Yes	Yes	Yes	Yes
Silterra 130nm	No	No	No	Yes	Yes	Yes
TSI 250nm BCD	No	No	Yes	Yes	Yes	Yes
Various JAZZ	No	No	No	Yes	Yes	Yes
Various XFAB	No	No	No	Yes	Yes	Yes

Key

Yes: If IP related, and IP publicly available
Yes: If IP related, but not publicly available. Contracted Design Services
No

This Table details our Process Experience, as well as type of experience with each node.